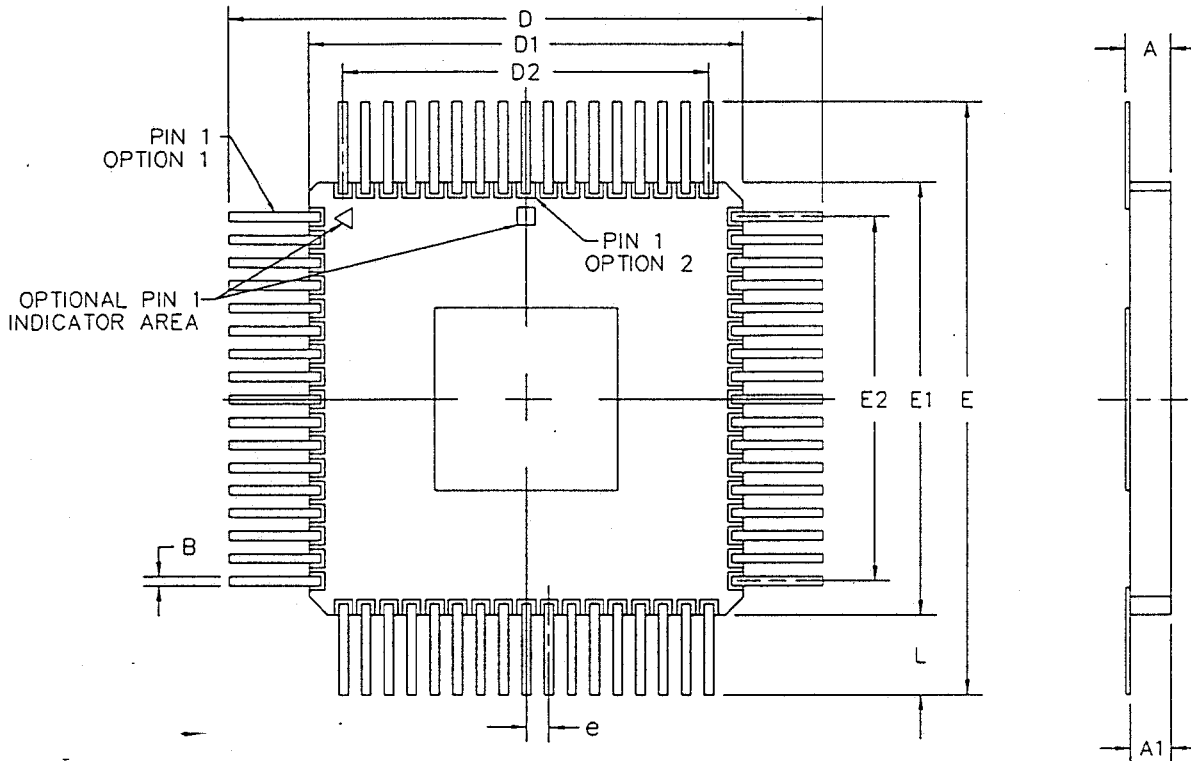


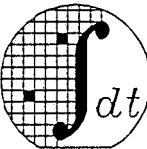
DCN	REV	DESCRIPTION	DATE	APPROVALS
17049	00	ORIGINAL ISSUE	11/30/89	D. G.
19350	01	CHANGE PIN 1 LOCATION	11/07/90	D. G.
28663	02	ADDED PIN 1 OPTION 2	2/26/96	<i>J. B. Bullitt</i>

MAR 03 1996



NOTES: (UNLESS OTHERWISE SPECIFIED)

1. ALL DIMENSIONS ARE IN INCHES.
2. BSC - BASIC LEAD SPACING BETWEEN CENTERS.
3. SYMBOL "N" REPRESENTS NUMBER OF LEADS.

SYMBOL			MIN	MAX	JEDEC		CONFIGURATION	EXCEPTIONS		
A			.080	.145	MIL-M-38510		MO-081-AA	NONE		
A1			.070	.090	TOLERANCES UNLESS OTHERWISE SPECIFIED FRAC DEC ANGLES ± - ± - ± -		NOT LISTED	N/A		
B			.016	.021	APPROVALS	DATE	 Integrated Device Technology, Inc. 3236 Scott Blvd., Santa Clara, CA 95051 (408) 727-6116 FAX: (408) 727-2328			
C			.008	.012	DRAWN <i>AA</i>	11/89				
D/E			1.640	1.870	CHECKED					
D1/E1			.926	.970						
D2/E2			.800 BSC				68 LD QUAD FLAT PACK MARKETING DRAWING			
e			.050 BSC				SCALE	SIZE	DRAWING NO.	REV
L			.350	.450			N/A	A	PSC-2085	02
N			68							
ND			17							
DO NOT SCALE DRAWING								SHEET 1 OF 1		