

## Description

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 9FGV1001B003NBGI. Please refer to the device datasheet for further information about the device.

## General Configuration

Parameter	Value	Units
Device I <sup>2</sup> C Address	Primary 0xD0	
Crystal Load Capacitance	8	pF

## Frequency Overview

Parameter	Configuration 0	Configuration 1	Configuration 2	Configuration 3	Units
Input	25	25	25	25	MHz
REF 0	—	—	—	—	MHz
REF 1	—	—	—	—	MHz
Output 0	50	100	125	156.25	MHz
Output 1	50	100	125	156.25	MHz
Output 2	50	100	125	156.25	MHz
Output 3	50	100	125	156.25	MHz

## Configuration 0 Parameters: SEL[1:0] = 00

Parameter	REF 0	REF 1	Output 0	Output 1	Output 2	Output 3	Units
Crystal Frequency	25	25	25	25	25	25	MHz
Default Output Status	Off	Off	On	On	On	On	
VDDO Voltage	3.3	3.3	3.3	3.3	3.3	3.3	V
Output Type	LVC MOS	LVC MOS	LVDS	LVDS	LVDS	LVDS	
Frequency	—	—	50	50	50	50	MHz
LP-HCSL Impedance	—	—	100	100	100	100	Ω

## Configuration 1 Parameters: SEL[1:0] = 01

Parameter	REF 0	REF 1	Output 0	Output 1	Output 2	Output 3	Units
Crystal Frequency	25	25	25	25	25	25	MHz
Default Output Status	Off	Off	On	On	On	On	
VDDO Voltage	3.3	3.3	3.3	3.3	3.3	3.3	V
Output Type	LVC MOS	LVC MOS	LVDS	LVDS	LVDS	LVDS	
Frequency	—	—	100	100	100	100	MHz
LP-HCSL Impedance	—	—	100	100	100	100	Ω

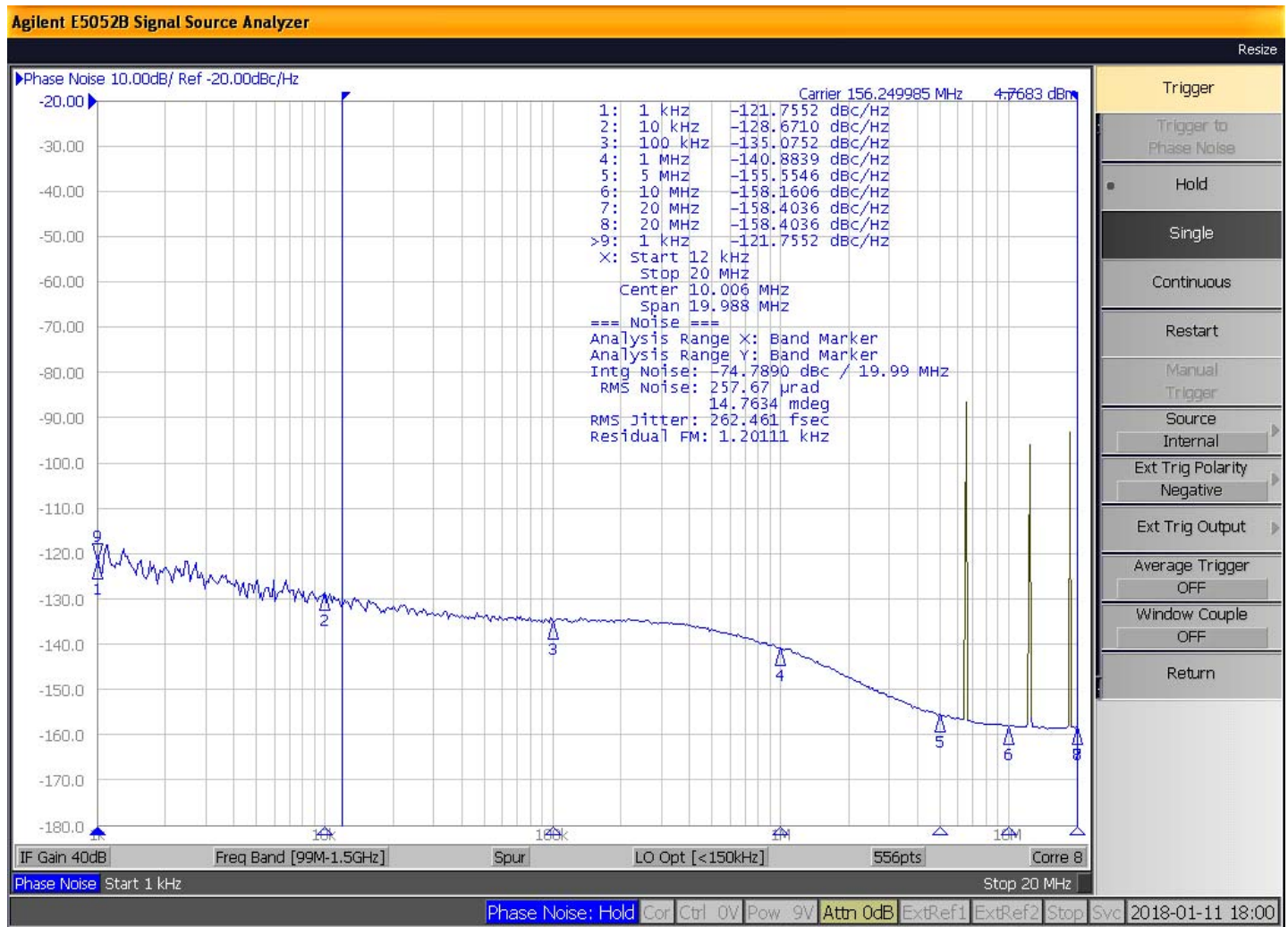
## Configuration 2 Parameters: SEL[1:0] = 10

Parameter	REF 0	REF 1	Output 0	Output 1	Output 2	Output 3	Units
Crystal Frequency	25	25	25	25	25	25	MHz
Default Output Status	Off	Off	On	On	On	On	
VDDO Voltage	3.3	3.3	3.3	3.3	3.3	3.3	V
Output Type	LVC MOS	LVC MOS	LVDS	LVDS	LVDS	LVDS	
Frequency	—	—	125	125	125	125	MHz
LP-HCSL Impedance	—	—	100	100	100	100	Ω

## Configuration 3 Parameters: SEL[1:0] = 11

Parameter	REF 0	REF 1	Output 0	Output 1	Output 2	Output 3	Units
Crystal Frequency	25	25	25	25	25	25	MHz
Default Output Status	Off	Off	On	On	On	On	
VDDO Voltage	3.3	3.3	3.3	3.3	3.3	3.3	V
Output Type	LVC MOS	LVC MOS	LVDS	LVDS	LVDS	LVDS	
Frequency	—	—	156.25	156.25	156.25	156.25	MHz
LP-HCSL Impedance	—	—	100	100	100	100	Ω

## Typical Phase Jitter at 156.25MHz



## 9FGV1001B003NBGI Ordering Information

Orderable Part Number	Marking	Package	Carrier Type	Temperature
9FGV1001B003NBGI	1003I YWW xxx	4 × 4 mm, 0.5mm pitch 24-VFQFPN	Tray	-40° to +85°C
9FGV1001B003NBGI8	1003I YWW xxx	4 × 4 mm, 0.5mm pitch 24-VFQFPN	Tape and Reel	-40° to +85°C

### Marking notes:

- <sup>1</sup> Line 1: truncated part number.
- <sup>2</sup> "YWW" is the last digit of the year and work week that the part was assembled.
- <sup>3</sup> "xxx" denotes the lot number.

## Revision History

Revision Date	Description of Change
May 15, 2018	Initial release.



Corporate Headquarters  
6024 Silver Creek Valley Road  
San Jose, CA 95138 USA  
[www.IDT.com](http://www.IDT.com)

Sales  
1-800-345-7015 or 408-284-8200  
Fax: 408-284-2775  
[www.IDT.com/go/sales](http://www.IDT.com/go/sales)

Tech Support  
[www.IDT.com/go/support](http://www.IDT.com/go/support)

DISCLAIMER Integrated Device Technology, Inc. (IDT) and its affiliated companies (herein referred to as "IDT") reserve the right to modify the products and/or specifications described herein at any time, without notice, at IDT's sole discretion. Performance specifications and operating parameters of the described products are determined in an independent state and are not guaranteed to perform the same way when installed in customer products. The information contained herein is provided without representation or warranty of any kind, whether express or implied, including, but not limited to, the suitability of IDT's products for any particular purpose, an implied warranty of merchantability, or non-infringement of the intellectual property rights of others. This document is presented only as a guide and does not convey any license under intellectual property rights of IDT or any third parties.

IDT's products are not intended for use in applications involving extreme environmental conditions or in life support systems or similar devices where the failure or malfunction of an IDT product can be reasonably expected to significantly affect the health or safety of users. Anyone using an IDT product in such a manner does so at their own risk, absent an express, written agreement by IDT.

Integrated Device Technology, IDT and the IDT logo are trademarks or registered trademarks of IDT and its subsidiaries in the United States and other countries. Other trademarks used herein are the property of IDT or their respective third party owners. For datasheet type definitions and a glossary of common terms, visit [www.idt.com/go/glossary](http://www.idt.com/go/glossary). Integrated Device Technology, Inc. All rights reserved.